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Japanese Laid-Open Utility Model Application Sho 61 - 44854

①

The shaded portions in the drawing indicate polycrystalline silicon wire, and portions that are not shaded indicate metal wire, respectively.

②

In the 2nd embodiment shown in Fig. 4, multiple portions of channel formation sections 12, 13, 15 and 16 of the polycrystalline silicon wire, which form gate electrodes that control channels, cause short circuits.

③

For the purpose of easily understanding metal wires that cause short circuits in Fig. 4, the metal wires are arranged on the field of a semiconductor substrate. However, it is also possible to arrange metal wires on polycrystalline silicon wires, which form the gate electrodes.